



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eiji Nishibe et al. Art Unit : 2826
Serial No. : 10/007,384 Examiner : Tan N. Tran
Filed : October 22, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF APRIL 22, 2003

Please amend the above-identified application as follows:

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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